AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claims 1-16 (cancelled).

17. (New) A method for monitoring an operating readiness of two memory elements assigned to an electronic unit, comprising:

monitoring a supply voltage of the electronic unit;

operating each of the memory elements using an operating voltage that is different compared to the supply voltage, each of the operating voltages being in a specified range so that the respective memory element is operational; and

detecting, based on the monitoring of the supply voltage, that the operating voltage of one of the two memory elements is not in the specified range, and processing program code from the other are of the two memory elements whose operating voltage is in the specified range.

18. (New) The method as recited in claim 17, wherein the monitoring of the supply voltage includes initially dividing down the supply voltage.

19. (New) The method as recited in claim 18, wherein the monitoring of the supply voltage includes cyclically measuring the divided down supply voltage using an analog-digital converter.

20. (New) The method as recited in claim 18, wherein the monitoring of the supply voltage includes cyclically reading in the divided down supply voltage at a general purpose input of an electronic computing unit.

21. (New) The method as recited in claim 18, wherein the monitoring of the supply voltage includes analyzing the divided down supply voltage at an interrupt input of an electronic computing unit.

22. (New) The method as recited in claim 19, wherein the monitoring of the supply voltage includes entering the supply voltage into a comparator whose output signal is analyzed.

23. (New) The method as recited in claim 22, wherein the monitoring of the supply voltage includes cyclically reading in the output signal of the comparator at a general purpose input of an electronic computing unit.

24. (New) The method as recited in claim 22, wherein the monitoring of the supply voltage includes analyzing the output signal of the comparator at an interrupt input of an electronic computing unit.

25. (New) The method as recited in claim 17, monitoring the operating voltage of the at least one of the memory elements.

26. (New) An electronic unit that is using a supply voltage, comprising:

two memory elements assigned to the electronic unit, each of the memory elements being operated using

an operating voltage that is different than the supply voltage, each of the operating voltages being in a respective, specified range so that the respective memory element is operational; and

a monitor to monitor the supply voltage, wherein based on the monitoring of the supply voltage, when the electronic unit detects that the operational voltage of one of the two memory elements is no longer in its respective specified range, program code is processed from the memory element when operating voltage is in its respective specified range.

27. (New) The electronic unit as recited in claim 26, further comprising:

an electronic computing unit.

28. (New) The electronic unit as recited in claim 26, wherein at least one of the memory elements is a flash memory element.

29. (New) The electronic unit as recited in claim 26, wherein the monitor includes an analog-digital converter to monitor the supply voltage.

30. (New) The electronic unit as recited in claim 26, wherein the monitor includes a comparator to monitor the supply voltage.

31. (New) A computer program, comprising:

program code configured to be executed on a computing unit, the program code configured to monitor an operational readiness of two memory elements assigned to an electronic unit, the program code, when executed by the computer unit, causing the computing unit to perform the following steps:

monitoring a supply voltage of the electronic unit;

operating each of the memory elements using an operating voltage that is different compared to the supply voltage, each of the operating voltages being in a specified range so that the respective memory element is operational; and

detecting, based on the monitoring of the supply voltage, that the operating voltage of one of the two memory elements is not in the specified range, and processing program code from the other are of the two memory elements whose operating voltage is in the specified range.

32. (New) A computer readable data medium storing a computer program, the computer program including:

program code configured to be executed on a computing unit, the program code configured to monitor an operational readiness of two memory elements assigned to an electronic unit, the program code, when executed by the computer unit, causing the computing unit to perform the following steps:

monitoring a supply voltage of the electronic unit;

operating each of the memory elements using an operating voltage that is different compared to the supply voltage, each of the operating voltages being in a specified range so that the respective memory element is operational;

detecting, based on the monitoring of the supply voltage, that the operating voltage of one of the two memory elements is not in the specified range, and processing program code from the other are of the two memory elements whose operating voltage is in the specified range.